

**REMARKS**

Claims 1-18 remain in the application for consideration of the Examiner.

Reconsideration and withdrawal of the outstanding rejections are respectfully requested in light of the above amendments and following remarks.

Turning now to the art rejections, Claims 1-7, 9-16, and 18 were rejected under 35 U.S.C. § 102(b) as being anticipated by Ibnabdeljalil; and Claims 8 and 17 were rejected under 35 U.S.C. § 103 as being unpatentable over Ibnabdeljalil in view of alleged Applicants admitted prior art (AAPA) in view of Sessions.

These rejections are respectfully traversed.

It is respectfully submitted that Ibnabdeljalil does not disclose or suggest the presently claimed invention including the first scribe seal around the integrated circuit to prevent mobile ions from entering the integrated circuit and the second scribe seal around the first scribe seal to provide mechanical strength to the integrated circuit in independent Claim 1, albeit defined as laying a first scribe seal around an integrated circuit to prevent mobility mobile ions from entering the integrated circuit and lying a second scribe seal around the first scribe seal to provide mechanical strength to the integrated circuit as defined in independent Claim 10.

Ibnabdeljalil discloses at column 4, lines 20-25 that electrically potential such as ground potential can be applied to the sacrificial structure effectively stopping the drift of unwanted charge particles.

Thus, Ibnabdeljalil discloses another way of solving the same problem.

Ibnabdeljalil sealing barriers only migrate against chemical impurities.

Applicants submit that if Ibnabdeljalil could prevent chemical impurities there would be no need for the electric potential.

The present invention eliminates the need for the electrical potential of Ibnabdeljalil.

Whether or not AAPA shows an integrated circuit having an analog portion and a digital portion and whether one of ordinary skill in the art would consider modifying Ibnabdeljalil is of no moment since the resulting construction would still in no way disclose or suggest the presently claimed invention.

Additionally, whether or not Sessions discloses a digital and analog circuits are conventionally combined on a single integrated circuit chip and whether one of ordinary skill in the art would consider modifying Ibnabdeljalil is of no moment again since the resulting construction would still in no way disclose or suggest the presently claimed invention.

In light of the above, it is respectfully submitted that the present application is in condition for allowance, and notice to that effect is respectfully requested.

While it is believed that the instant response places the application in condition for allowance, should the Examiner have any further comments or suggestions, it is respectfully requested that the Examiner contact the undersigned in order to expeditiously resolve any outstanding issues.

To the extent necessary, Applicant petitions for an Extension of Time under 37 CFR 1.136. Please charge any fees in connection with the filing of this paper, including extension of time fees, to the deposit account of Texas Instruments Incorporated, Account No. 20-0668.

Respectfully submitted,



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